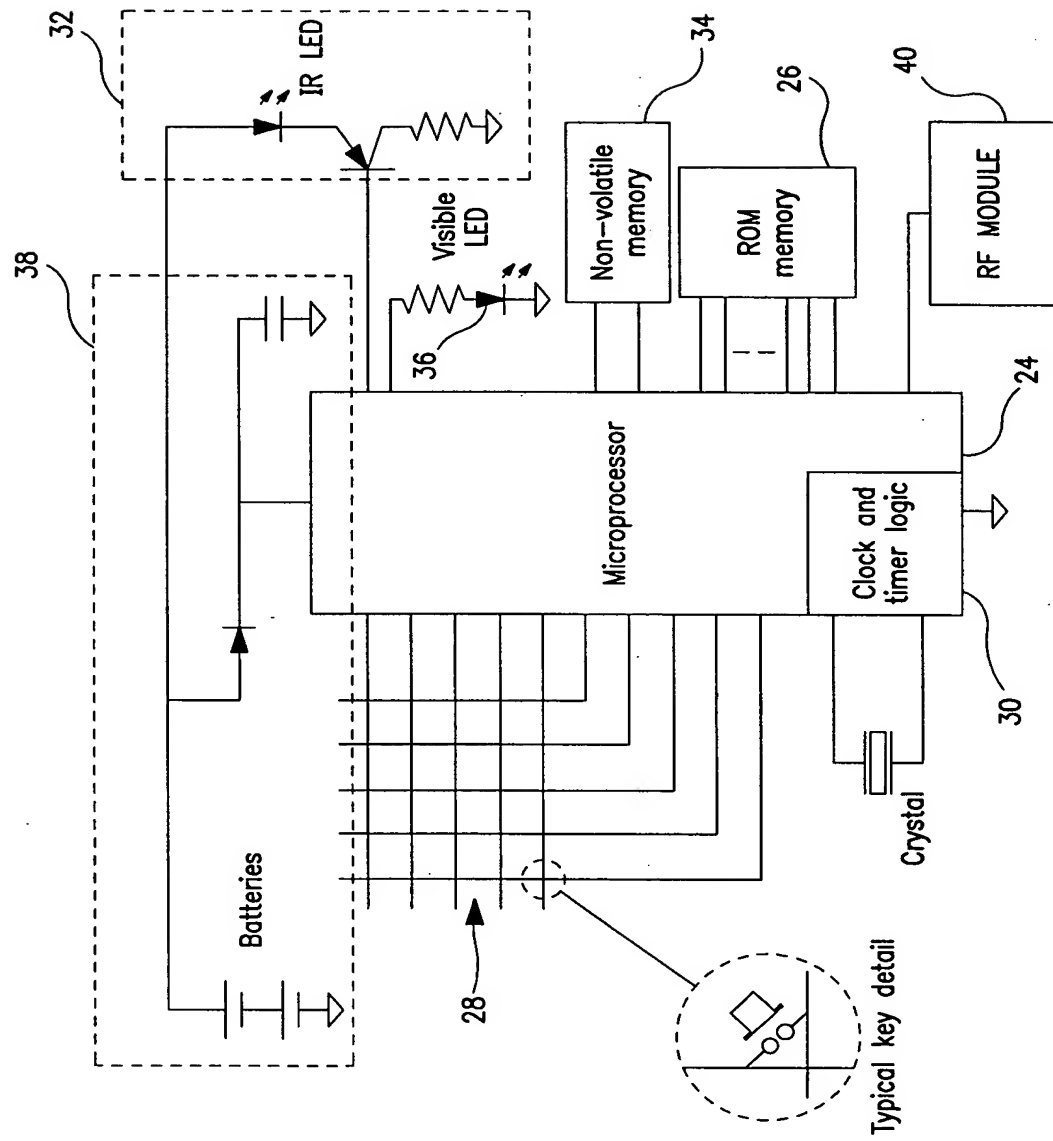


FIG. 1



REMOTE CONTROL HARDWARE BLOCK DIAGRAM

FIG. 2

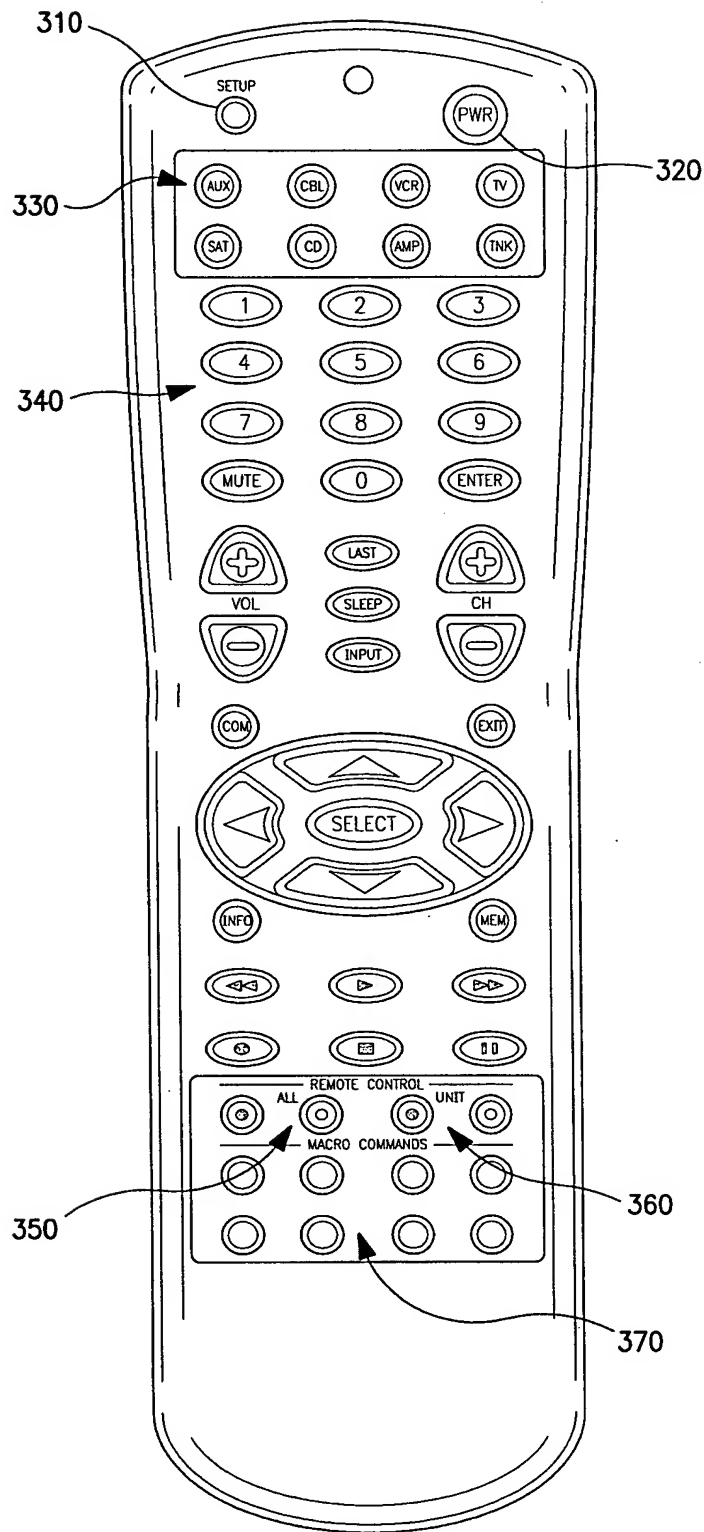


FIG. 3

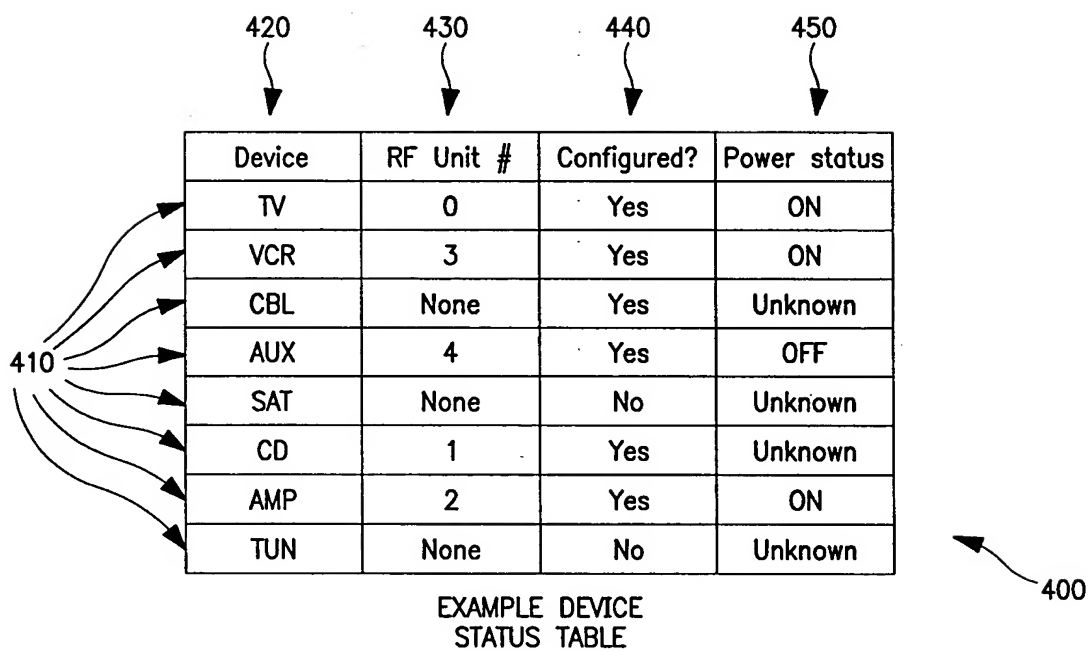


FIG. 4

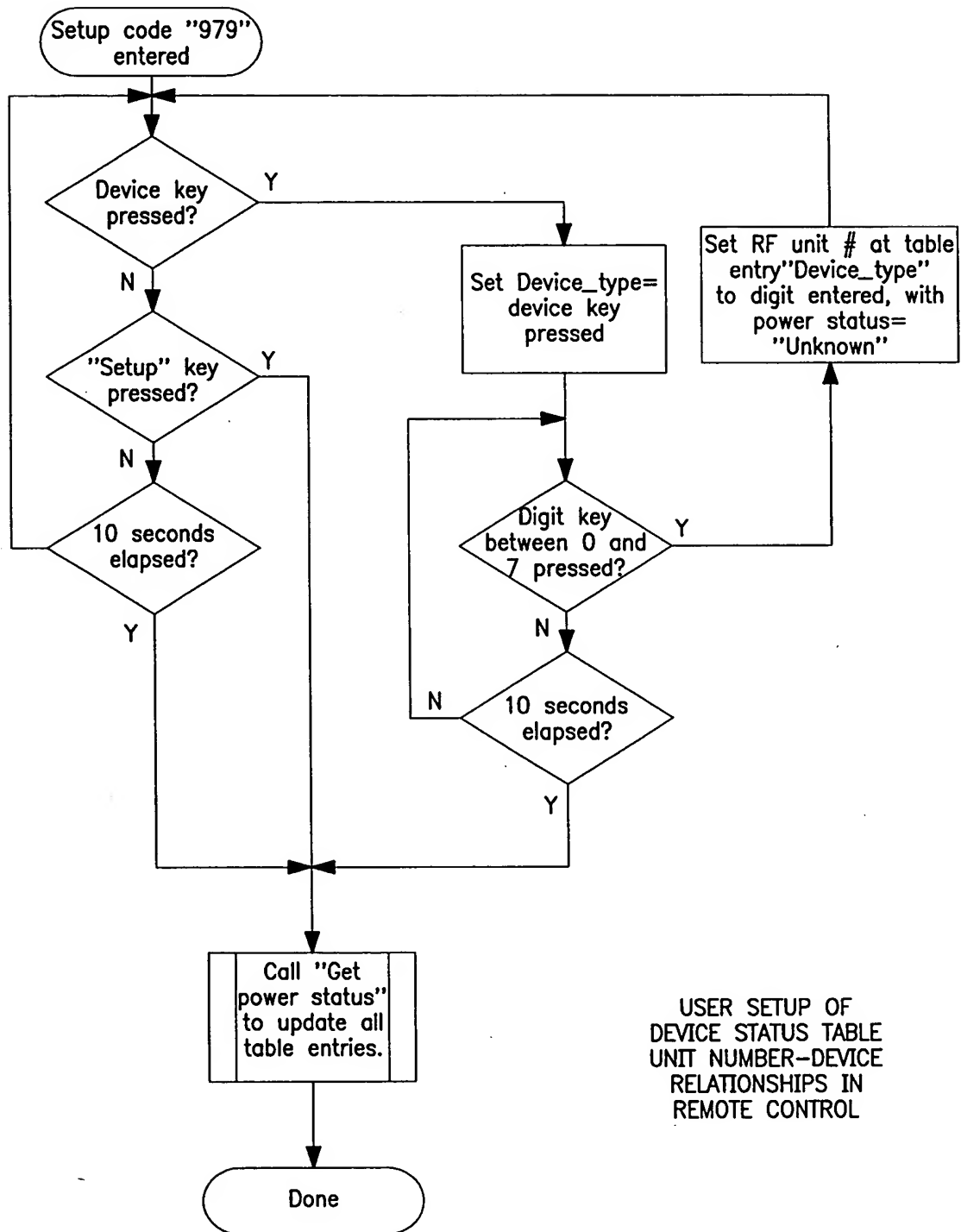
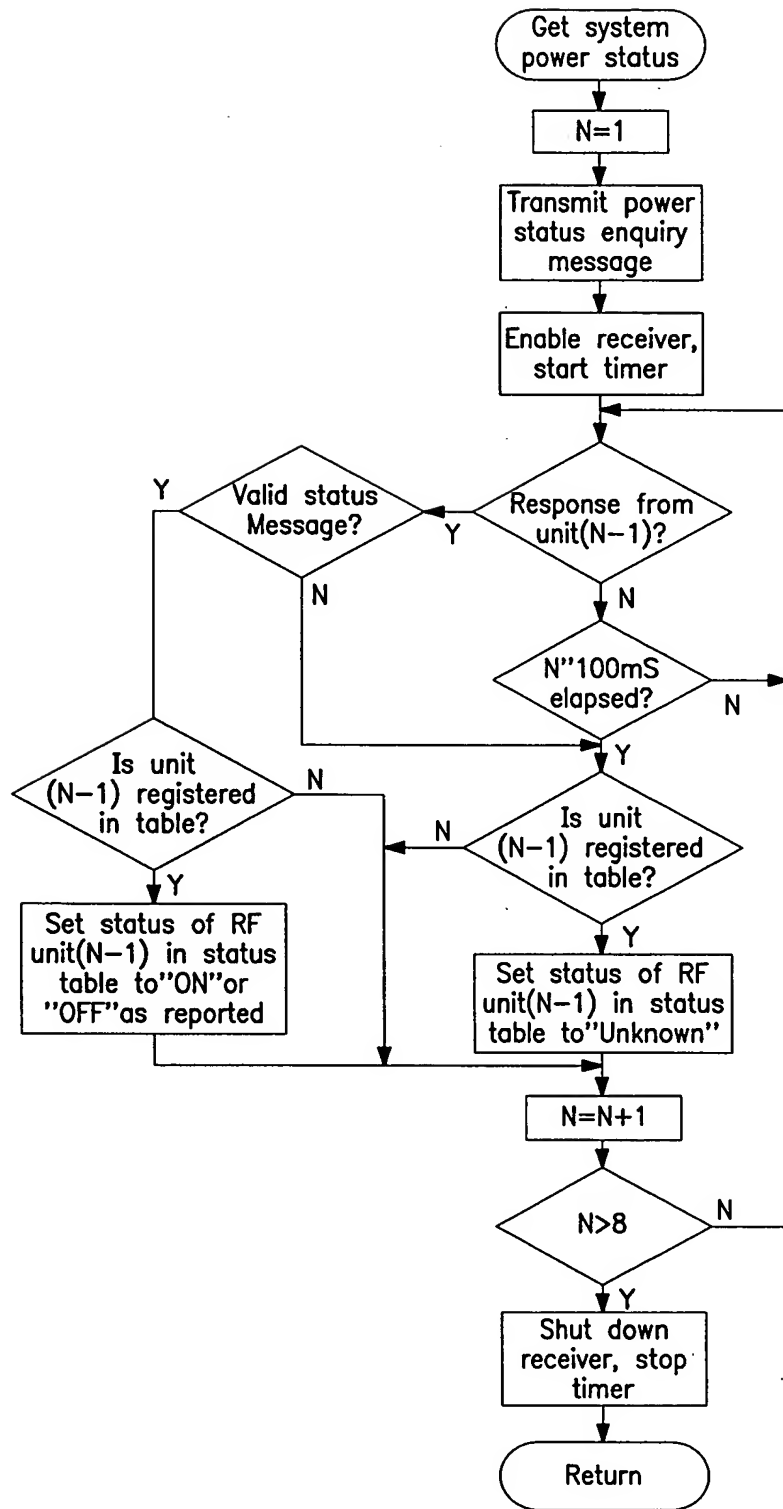
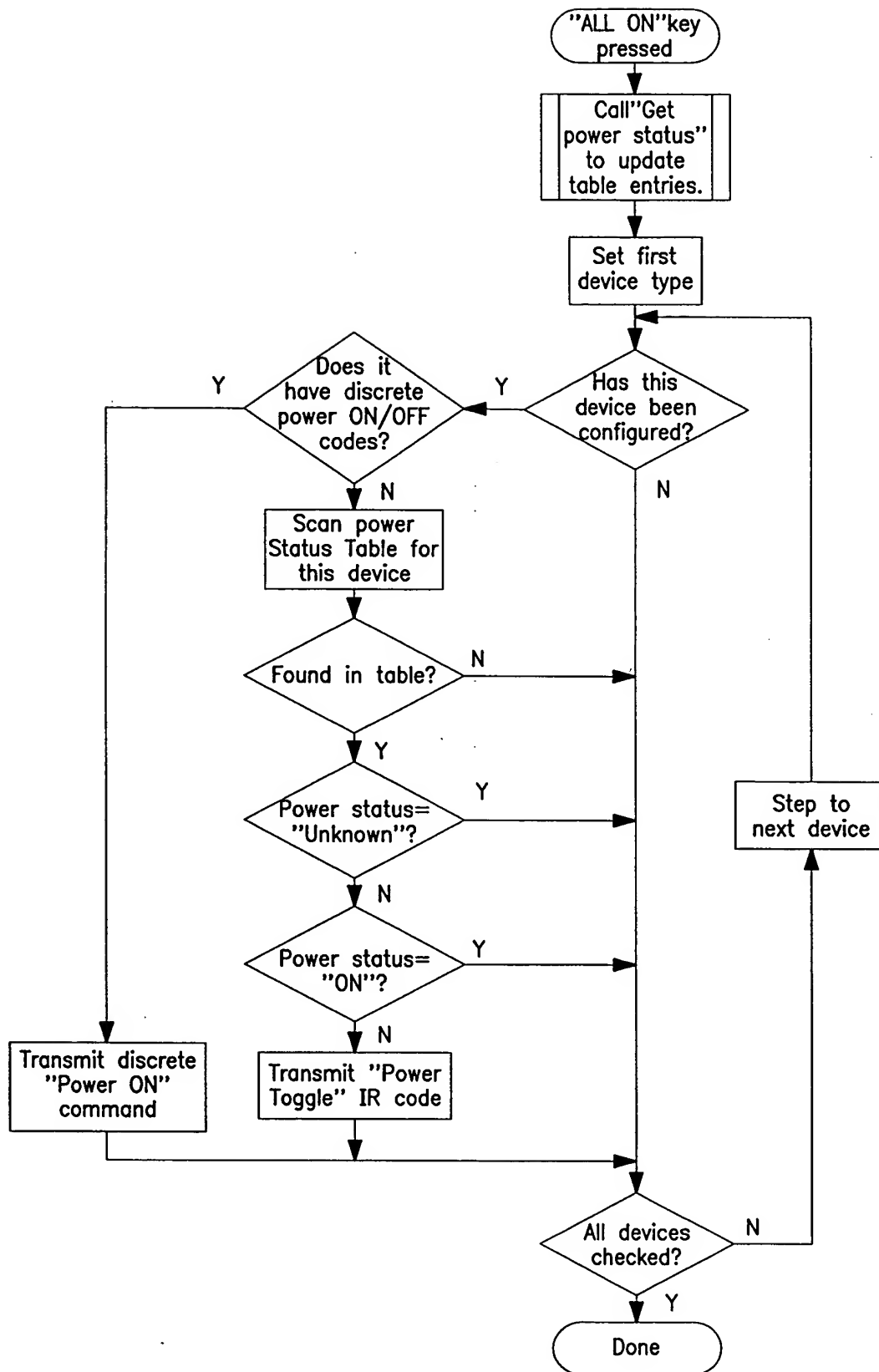


FIG. 5



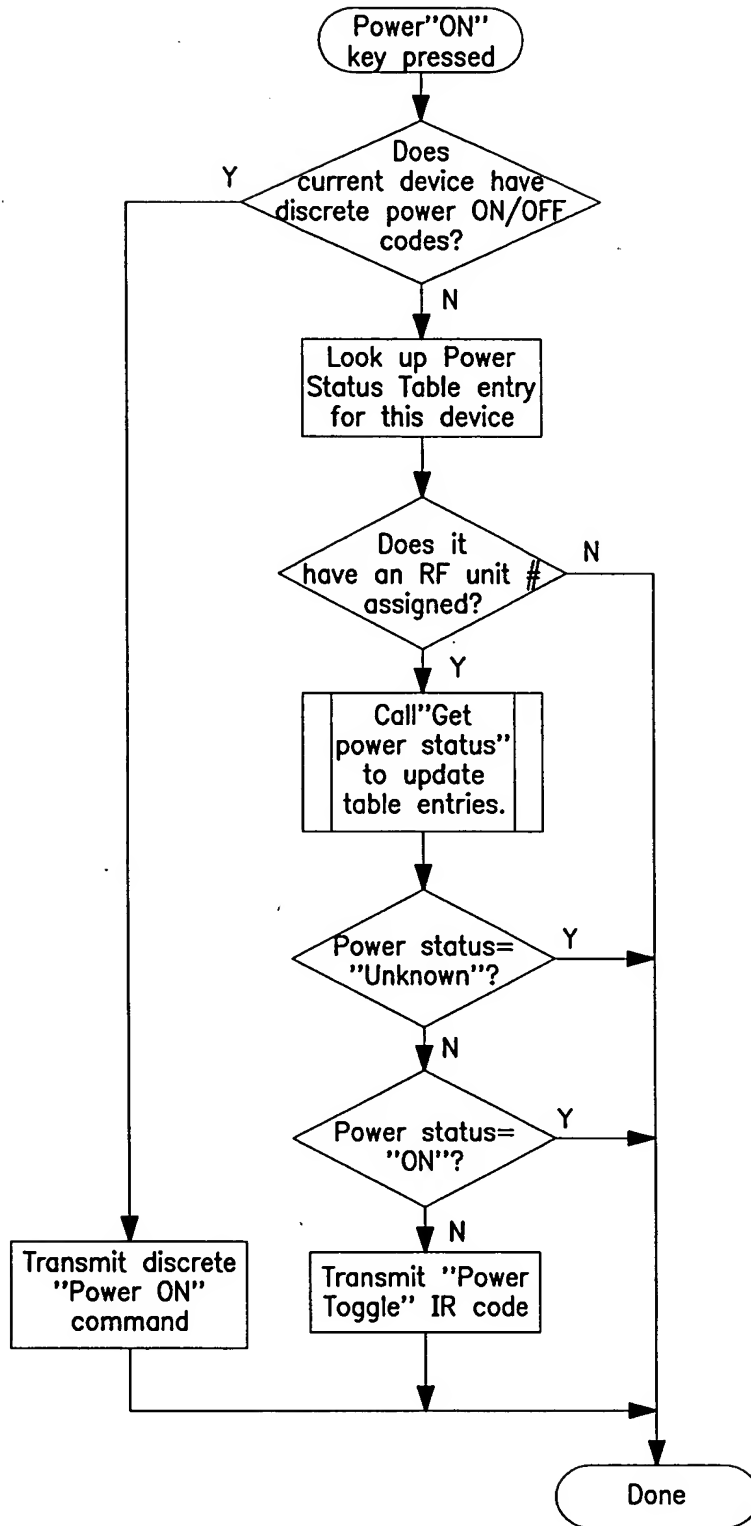
REMOTE CONTROL "GET POWER STATUS" LOGIC FLOW

FIG. 6



REMOTE CONTROL "ALL ON" LOGIC

FIG. 7



REMOTE CONTROL INDIVIDUAL "UNIT ON" LOGIC

FIG. 8



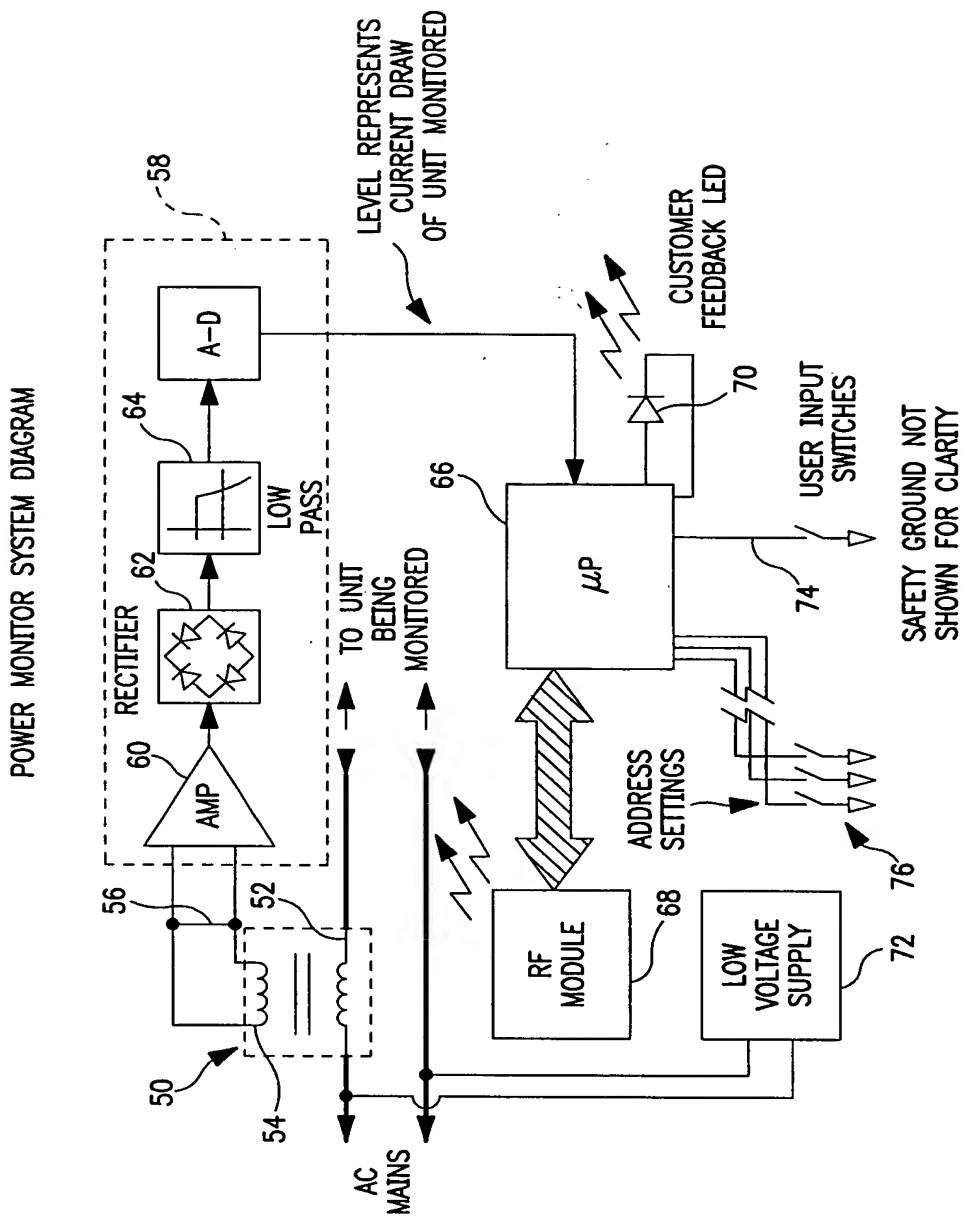


FIG. 9

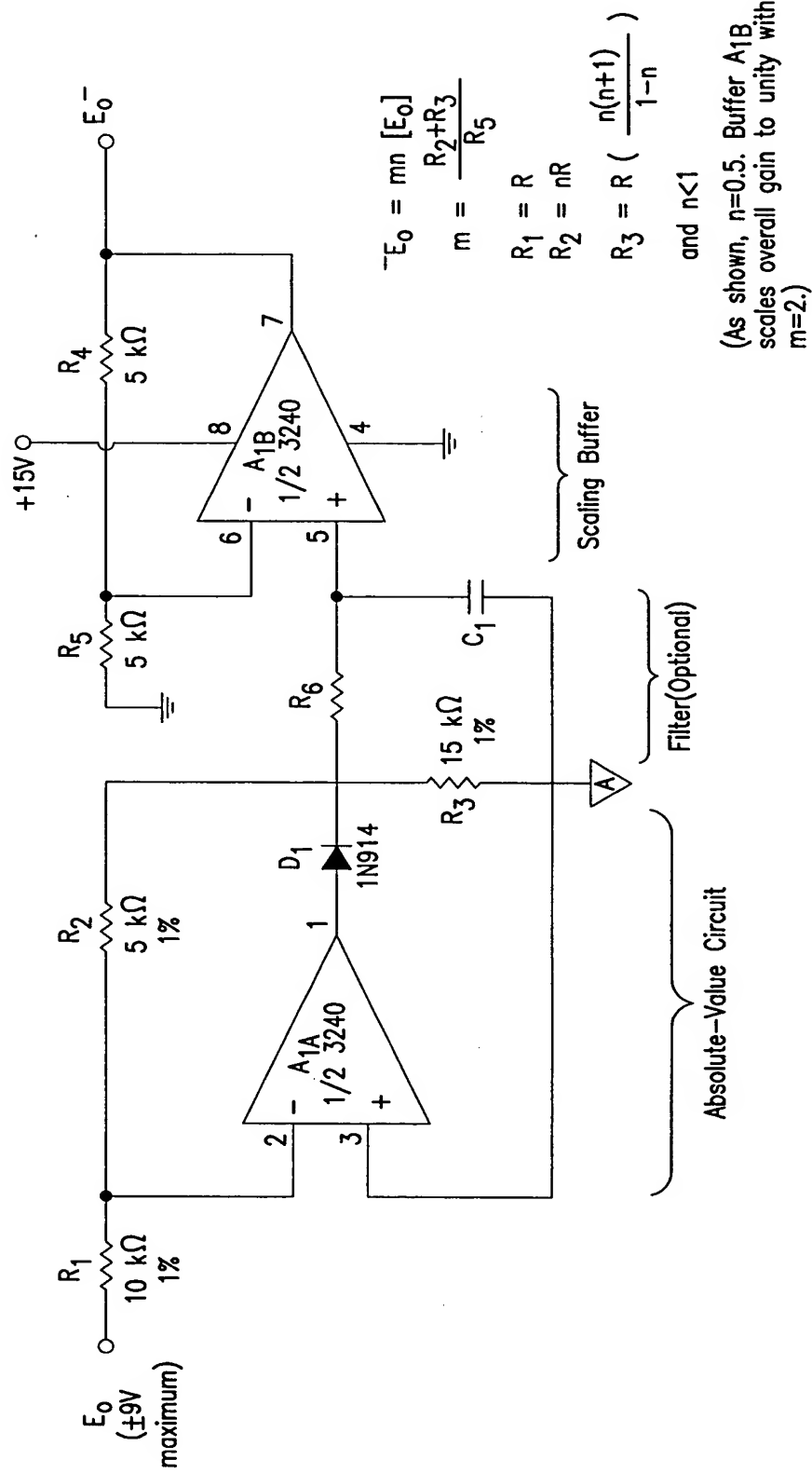
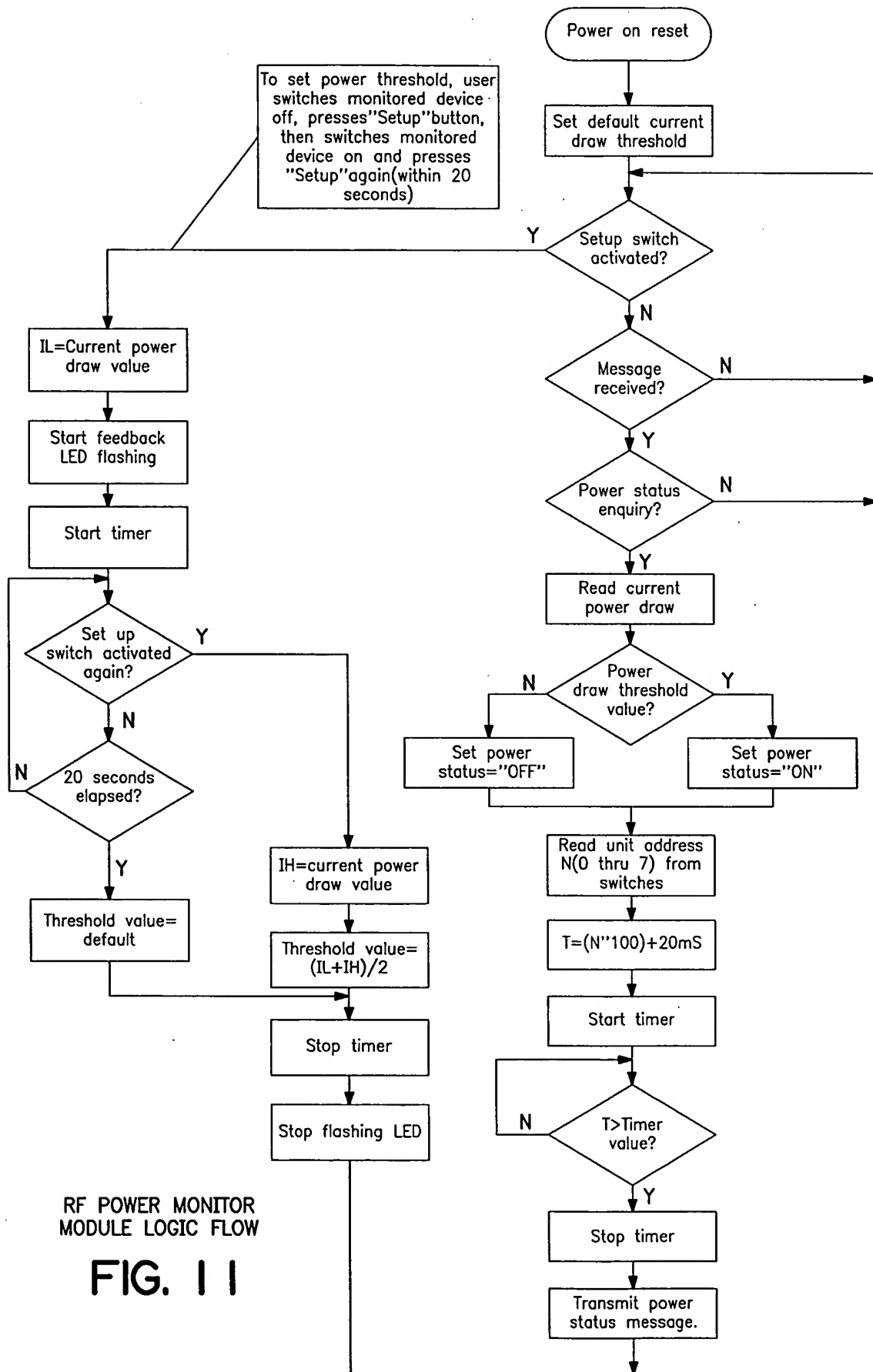


FIG. 10



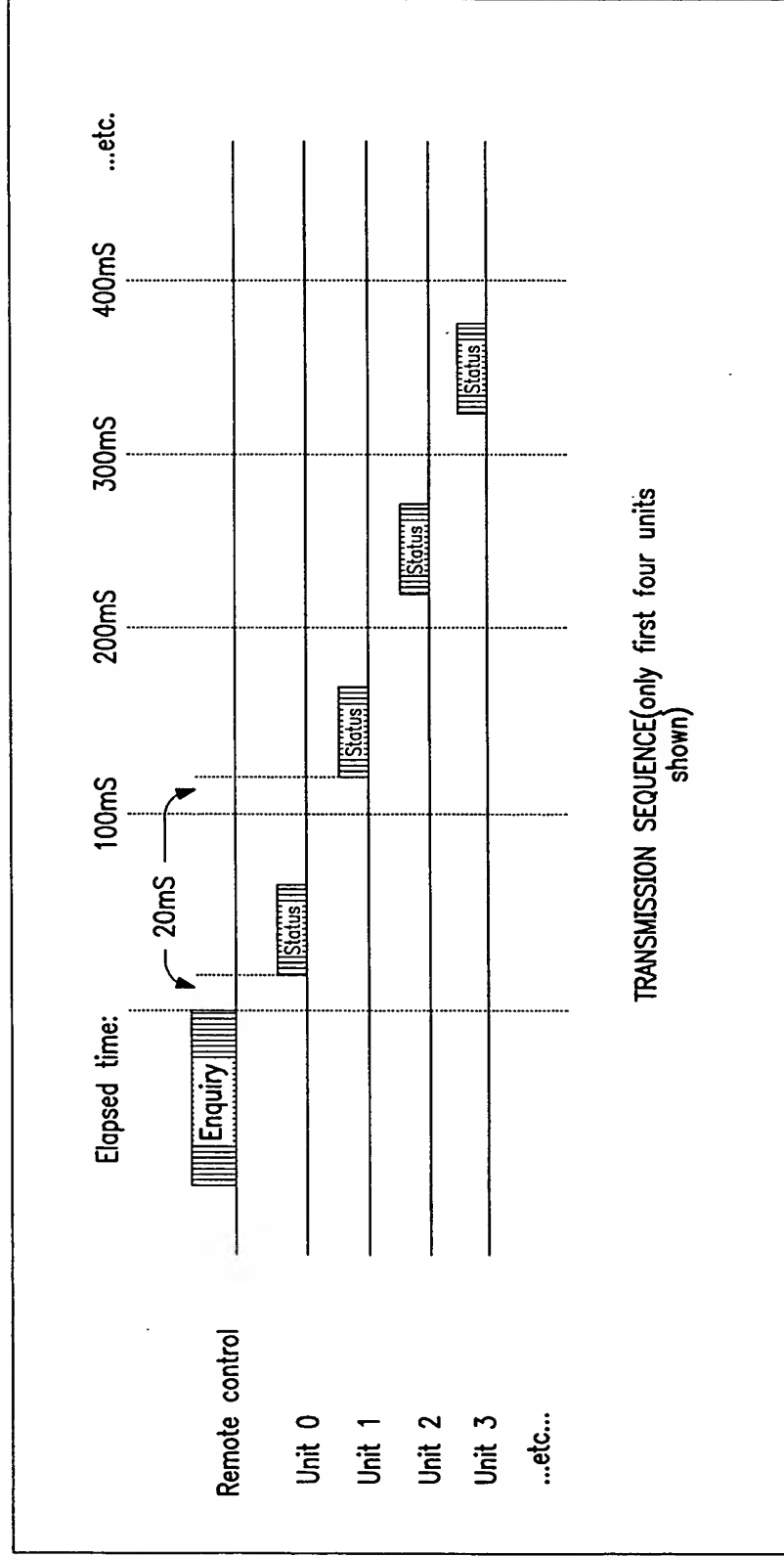


FIG. 12

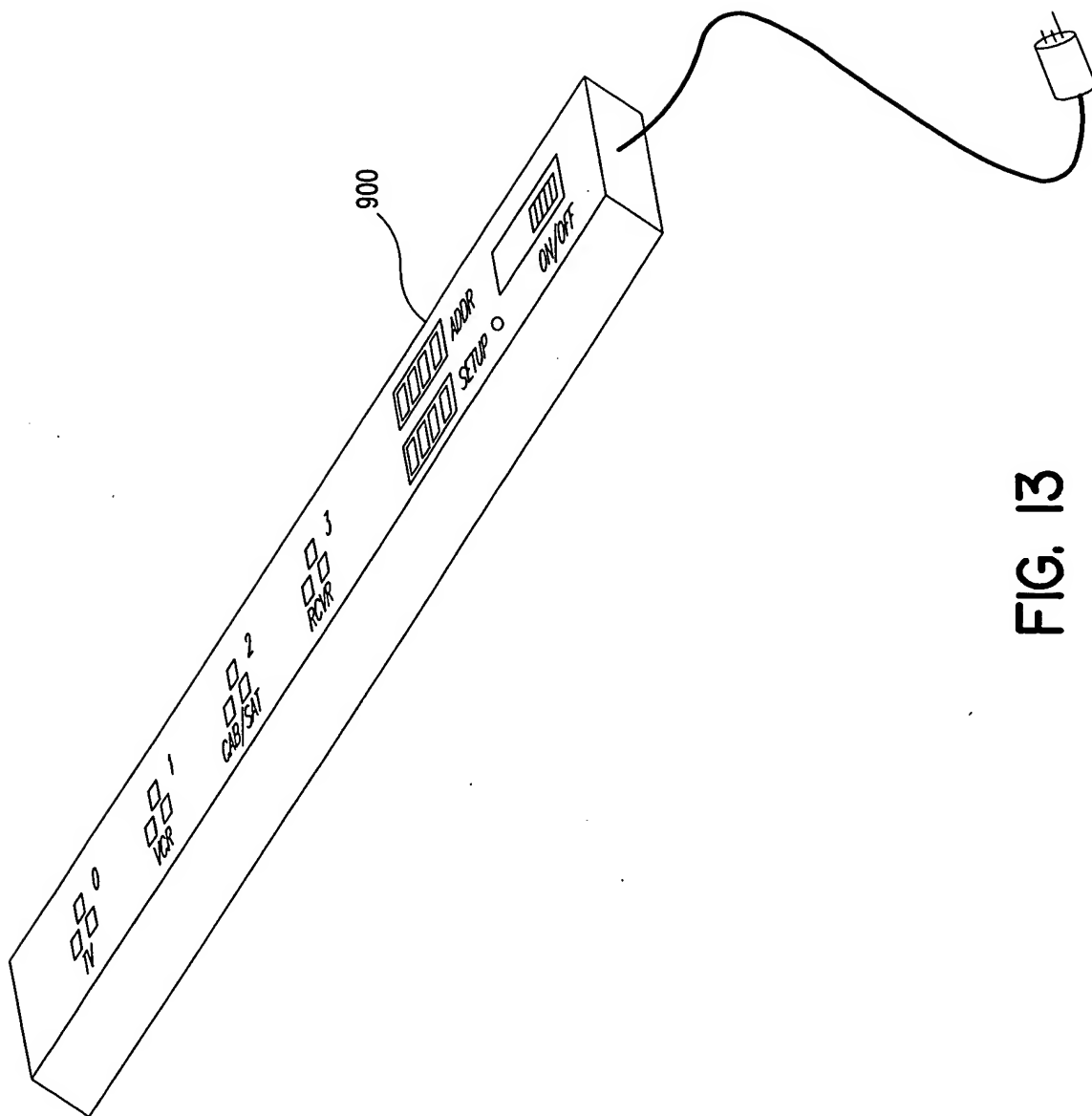


FIG. 13

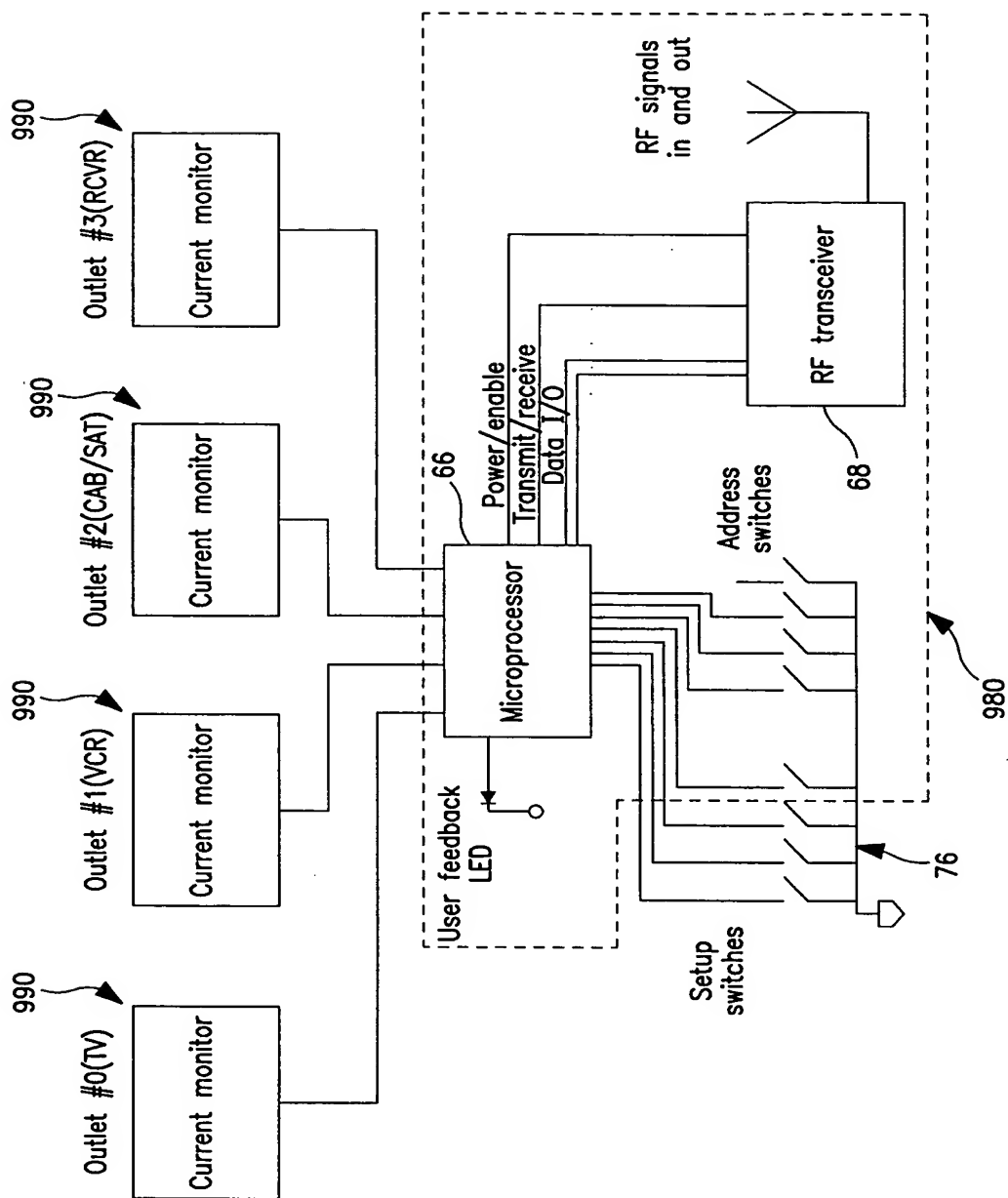


FIG. 14

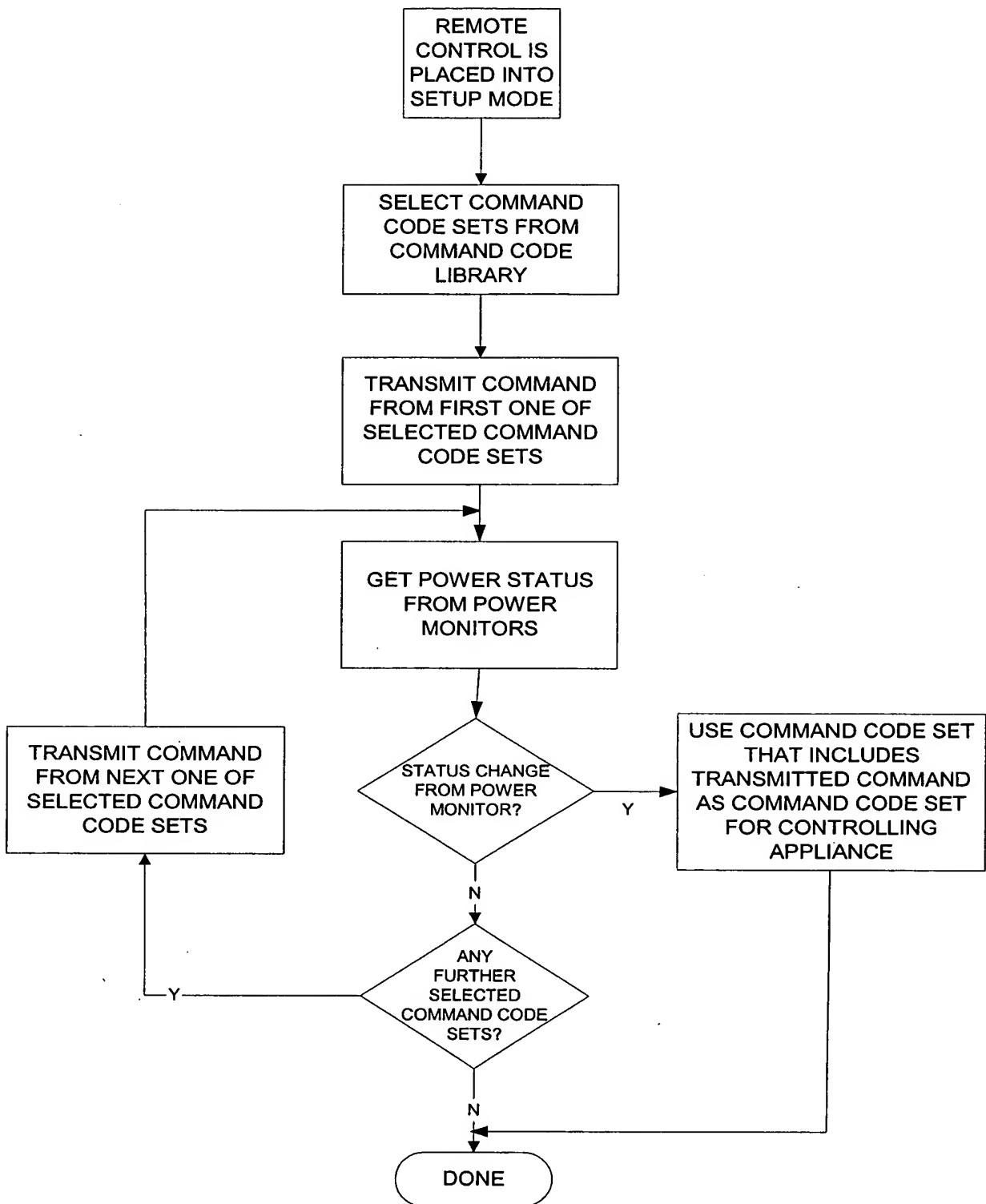


FIGURE 15